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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRE APPLICATION OF:

Masaaki TAKATA, et al.

GROUP: 3749

SERIAL NO: 10/809,705

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EXAMINER:

FOR: THERMAL TREATMENT SYSTEM FOR SEMICONDUCTORS

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

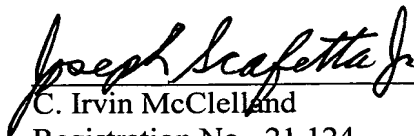
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the Assignment for Published Patent Application information.

Respectfully Submitted,

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